



TOP VIEW

R

# BC847AT, BT, CT

SOT-523 Min

0.15

0.75

1.45

0.90

1.50

0.00

0.60

0.10

0.10

0.45

0

Max

0.30

0.85

1.75

1.10

1.70

0.10

0.80

0.30

0.20

0.65

8

All Dimensions in mm

Тур

0.22

0.80

1.60

0.50

1.00

1.60

0.05

0.75

0.22

0.12

0.50

Dim

Α

В

С

D

G

Н

J

Κ

L

Μ

Ν

# NPN SMALL SIGNAL SURFACE MOUNT TRANSISTOR

## Features

**Epitaxial Die Construction** Complementary PNP Type Available (BC857AT,BT,CT) Ultra-Small Surface Mount Package Lead Free/RoHS Compliant (Note 2)

## **Mechanical Data**

#### Case: SOT-523

Case Material - Molded Plastic. UL Flammability Classification Rating 94V-0 Moisture Sensitivity: Level 1 per J-STD-020C Terminals: Solderable per MIL-STD-202, Method 208 Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).

Terminal Connections: See Diagram

Marking Code: See Table Below & Diagram on Page 2

Ordering Information: See Page 2 Date Code Information: See Page 2

Weight: 0.002 grams (approximate)

Туре	Marking
BC847AT	1E
BC847BT	1F
BC847CT	1M

#### @ $T_A = 25^{\circ}C$ unless otherwise specified **Maximum Ratings**

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	45	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V
Collector Current	lc	100	mA
Power Dissipation (Note 1)	Pd	150	mW
Thermal Resistance, Junction to Ambient (Note 1)	R <sub>JA</sub>	833	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

2. No purposefully added lead.



# Electrical Characteristics @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition		
DC Current Gain (Note 3)	Current Gain A B C Current Gain A B C	hfe	— — 110 200 420	 270  290 520	  220 450 800	_	$V_{CE} = 5.0V, I_{C} = 2.0mA$		
Collector-Emitter Saturation Voltage	(Note 3)	V <sub>CE(SAT)</sub>	_	_	250 600	mV	$\begin{array}{l} I_C = 10 m \text{A}, \ I_B = 0.5 m \text{A} \\ I_C = 100 m \text{A}, \ I_B = 5.0 m \text{A} \end{array}$		
Base-Emitter Saturation Voltage	(Note 3)	V <sub>BE(SAT)</sub>	—	700 900	_	mV	$\begin{array}{l} I_C = 10 m \text{A}, \ I_B = 0.5 m \text{A} \\ I_C = 100 m \text{A}, \ I_B = 5.0 m \text{A} \end{array}$		
Base-Emitter Voltage	(Note 3)	V <sub>BE</sub>	580 —	660 —	700 770	mV	$V_{CE} = 5.0V, I_C = 2.0mA$ $V_{CE} = 5.0V, I_C = 10mA$		
Collector-Emitter Cutoff Current	(Note 3)	I <sub>CBO</sub> I <sub>CBO</sub>	—	—	15 5.0	nΑ μΑ	$V_{CB} = 30V$ $V_{CB} = 30V$ , $T_A = 150^{\circ}C$		
Gain Bandwidth Product		f <sub>T</sub>	100	_	_	MHz	$\label{eq:Vce} \begin{array}{l} V_{CE}=5.0V,\ I_{C}=10mA,\\ f=100MHz \end{array}$		
Output Capacitance		C <sub>OBO</sub>	_	_	4.5	pF	$V_{CB} = 10V, f = 1.0MHz$		
Noise Figure	NF	_	_	10 4.0	dB	V <sub>CE</sub> = 5V, R <sub>S</sub> = 2.0k f = 1.0kHz, BW = 200Hz			

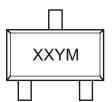
Notes: 3. Short duration pulse test used to minimize self-heating effect.

# Ordering Information (Note 4)

Device	Packaging	Shipping			
BC847AT-7-F	SOT-523	3000/Tape & Reel			
BC847BT-7-F	SOT-523	3000/Tape & Reel			
BC847CT-7-F	SOT-523	3000/Tape & Reel			

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

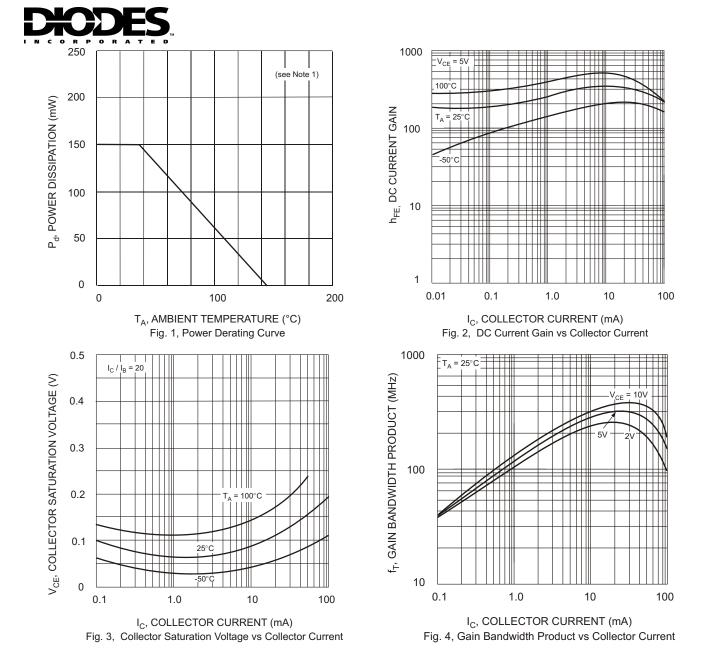
# **Marking Information**



 $\begin{array}{l} XX = Product Type \mbox{ Marking Code (See Page 1), e.g. 1E = BC847AT \\ YM = Date Code \mbox{ Marking } \\ Y = Year \mbox{ (ex: N = 2002)} \\ M = Month \mbox{ (ex: 9 = September)} \end{array}$ 

Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	Κ	L	М	N	Р	R	S	Т	U	V	W	Х	Y	Z
Ν	Nonth		Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	0	ct	Nov	Dec
Code			1	2	2	4	5	6	7		0	6	<u> </u>	Ν	D



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